

Attorney's Docket No. 0325.00409



G-AU 26672
1-25-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: S. Babar Raza et al.
Serial No.: 09/676,706 Art Unit: 2661
Filed: September 29, 2000 Examiner:
For: LOGIC FOR GENERATING MULTICAST/UNICAST ADDRESS(ES)

PATENT RECEIVED
JAN 24 2001
Technology Center 2600

Assistant Commissioner For Patents
Washington, D.C. 20231

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
WITHIN THREE MONTHS OF FILING OR
BEFORE MAILING OF FIRST OFFICE ACTION (37 CFR 1.97(b))**

NOTE: "An information disclosure statement shall be considered by the Office if filed: (1) within three months of the filing date of a national application; (2) within three months of the date of entry of the national stage as set forth in § 1.491 in an international application; or (3) before the mailing date of a first Office action on the merits, whichever event occurs last." 37 CFR 1.97(b).

**IDENTIFICATION OF TIME OF FILING THE ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT**

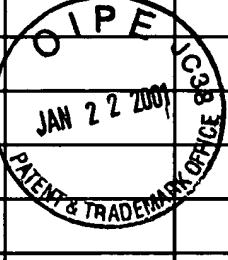
The information disclosure statement submitted herewith is being filed within three months of the filing date of the application or date of entry into the national stage of an international application before the mailing date of a first Office action on the merits, whichever event occurs last. 37 CFR 1.97(b).

CHRISTOPHER P. MAIORANA, P.C.

Christopher P. Maiorana
Registration No. 42,829
24025 Greater Mack, Suite 200
St. Clair Shores, MI 48080
(810) 498-0670
Dated: January 19, 2001

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231, on January 19, 2001.

By: Mary Donna Berkley
Mary Donna Berkley

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 0325.00409	SERIAL NO. 09/676,706		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT: S. Babar Raza et al.			
		FILING DATE September 29, 2000	GROUP 2661		
U.S. PATENT DOCUMENTS					
Examiner Initial	Document No.	Date	Name	Class	Sub-Class
					
FOREIGN PATENT DOCUMENTS					
	Document No.	Date	Country	Class	Sub-Class
OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)					
	S. Babar Raza et al., "ARCHITECTURE FOR IMPLEMENTING VIRTUAL MULTIQUEUE FIFO", U.S. Serial No. 09/676,704, Filed September 29, 2000.				
	S. Babar Raza, et al., "LOGIC FOR PROVIDING ARBITRATION FOR SYNCHRONOUS DUAL-PORT MEMORY", U.S. Serial No. 09/676,169, Filed September 29, 2000.				
	S. Babar Raza et al., "LOGIC FOR INITIALIZING THE DEPTH OF THE QUEUE POINTER MEMORY", U.S. Serial No. 09/676,705, Filed September 29, 2000.				
	S. Babar Raza et al., "METHOD AND LOGIC FOR INITIALIZING THE FORWARD-POINTER MEMORY DURING NORMAL OPERATION OF THE DEVICE AS A BACKGROUND PROCESS", U.S. Serial No. 09/676,170, Filed September 29, 2000.				
	S. Babar Raza et al., "METHOD FOR LOGIC FOR STORING AND EXTRACTING IN-BAND MULTICAST PORT INFORMATION STORED ALONG WITH THE DATA IN A SINGLE MEMORY WITHOUT MEMORY READ CYCLE OVERHEAD", U.S. Serial No. 09/676,171, Filed September 29, 2000.				
Examiner		Date Considered			
Examiner: Initial if references considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					